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Roark, R.M.; Escabi, M.A.;

[Signal Processing, IEEE Transactions on \[see also Acoustics, Speech, and Signal Processing, IEEE Volume 47, Issue 3, March 1999 Page\(s\):701 - 716](#)  
Digital Object Identifier 10.1109/78.747777[AbstractPlus](#) | [References](#) | Full Text: [PDF\(540 KB\)](#) [IEEE JNL](#)  
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Hegde, R.; Shenoi, B.A.;

[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on \[see also Circuits Express Briefs, IEEE Transactions on\]](#)  
Volume 45, Issue 11, Nov. 1998 Page(s):1476 - 1486  
Digital Object Identifier 10.1109/82.735359[AbstractPlus](#) | [References](#) | Full Text: [PDF\(396 KB\)](#) [IEEE JNL](#)  
[Rights and Permissions](#) **3. Universal maximally flat lowpass FIR systems**

Samadi, S.; Nishihara, A.; Iwakura, H.;

[Signal Processing, IEEE Transactions on \[see also Acoustics, Speech, and Signal Processing, IEEE Volume 48, Issue 7, July 2000 Page\(s\):1956 - 1964](#)  
Digital Object Identifier 10.1109/78.847782[AbstractPlus](#) | [References](#) | Full Text: [PDF\(220 KB\)](#) [IEEE JNL](#)  
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Vaidyanathan, P.;

[Circuits and Systems, IEEE Transactions on](#)

Volume 32, Issue 3, Mar 1985 Page(s):236 - 244

[AbstractPlus](#) | Full Text: [PDF\(888 KB\)](#) [IEEE JNL](#)  
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Selesnick, I.W.;

[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on \[see also Circuits Express Briefs, IEEE Transactions on\]](#)  
Volume 49, Issue 3, March 2002 Page(s):219 - 223

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